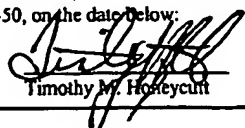


List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	AMDI:132\HON	10/676,904
	APPLICANT James F. Buller et al.	CERTIFICATE OF MAILING 37 C.F.R. 1.8 I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below: <div style="display: flex; justify-content: space-between;"> <div>Date <u>January 2, 2004</u></div> <div> Timothy M. Honeycutt</div> </div>
	FILING DATE October 1, 2003	GROUP 2823

U.S. PATENT DOCUMENTS


EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
WP	A1	6,074,906	June 13, 2000	Jon Cheek et al.	438	231	
WP	A2	US 6,506,642 B1	January 14, 2003	Scott Luning et al.	438	231	

FOREIGN PATENT DOCUMENTS

EXAM INIT.	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION

OTHER ART

EXAM INIT.	REF. DES.	AUTHOR; TITLE; JOURNAL; VOLUME; DATE; PERTINENT PAGES
WP	A3	Dallas Semiconductor; <i>Compensating for Ideality Factor and Series Resistance Differences between Thermal Sense Diodes</i> ; www.maxim-ic.com/an1057 ; April 17, 2002; pp. 1-2
WP	A4	Intel; <i>Intel Pentium® III Processor, Thermal Diode</i> ; http://support.intel.com/support/processors/pentiumiii/sb/CS-007590.htm ; August 12, 2003; pp. 1-3

EXAMINER 	DATE CONSIDERED <u>04/04/05</u>
--	---------------------------------

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement--PTO-1449 (Modified)